

NON-LINEAR DECISION FEEDBACK PHASE LOCKED LOOP FILTER**Abstract of the Disclosure**

Systems, devices and methods are provided for precisely and responsively
5 compensating for deterministic jitter. One aspect of the present subject matter is a
circuit for improving a phase lock of a timing signal for a receiver. One
embodiment includes a sample and hold circuit, a decision logic module, and a
switch. The sample and hold circuit is adapted to receive and hold a phase signal
that represents a signal from a phase detector of the receiver. The switch is adapted
10 to controllably pass the phase signal from the sample and hold circuit. The passed
phase signal is capable of being used in adjusting the timing signal for the receiver.
The decision logic module is adapted to detect good signal transitions, and actuate
the switch to pass the phase signal for good signal transitions. Other aspects are
provided herein.

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